

ODIN Q_JTAGHUB V1 bus (GPIO)

```
# GPIO bus GPIO[7] = RESET
# GPIO bus GPIO[6] = WRENB
# GPIO bus GPIO[5:4] = ADDR
# GPIO bus GPIO[3:0] = DATA
# JTAG HUB register[3:0](0x0) : FPGAs
# JTAG HUB register[7:0](0x1) : EPCs
# JTAG HUB register[8](0x2) : GbE
# JTAG HUB register[9](0x2) : nTRST to GbE
# JTAG HUB register[11](0x2) : EXT/INT JTAG
```

INPUT/OUTPUT register: 0x14

DATA register: 0x10

Set all GPIO lines output: 0xFF

Reset Q_JTAGHUB_V1: 0x00, 0x80, 0x00

Program EPCs:

Set external : 0x28, 0x68, 0x28

Set internal: 0x20, 0x60, 0x20

Program Q_MP_epc: 0x11, 0x51, 0x11

Program Q_L0_epc: 0x12, 0x52, 0x12

Program Q_L1_epc: 0x14, 0x54, 0x14

Program Q_FE_epc: 0x18, 0x58, 0x18

Program Q_MP: 0x01, 0x41, 0x01

Program Q_L0: 0x02, 0x42, 0x02

Program Q_L1: 0x04, 0x44, 0x04

Program Q_FE: 0x08, 0x48, 0x08