

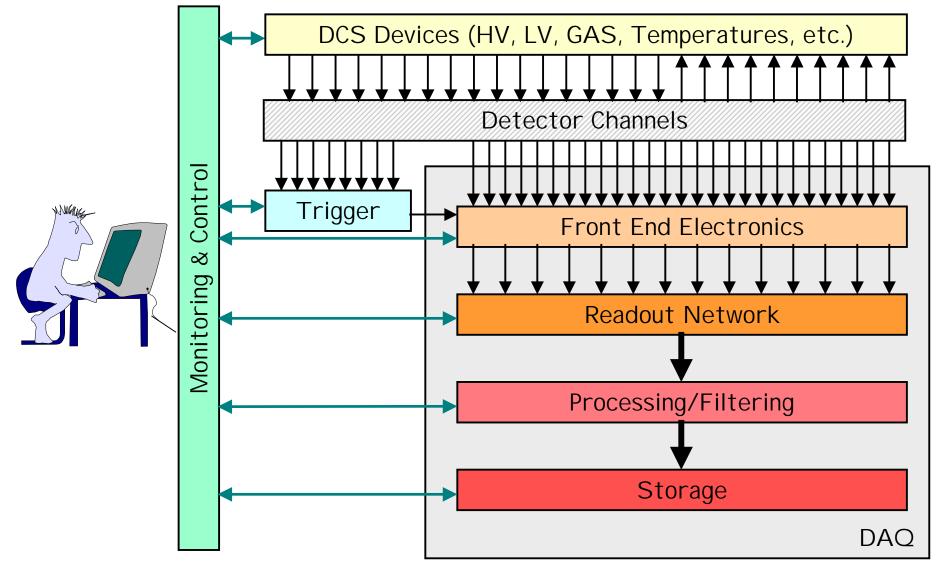
LHCB's

Experiment Control System

Kick Experiment Control

- In charge of the Control and Monitoring of:
 - Detector Operations (ex Slow Controls)
 - I GAS, HV, LV, temperatures...
 - Data Acquisition and Trigger
 - I FE Electronics, Event building, EFF, etc.
 - Experimental Infrastructures
 - I Cooling, ventilation, electricity distribution, ...
 - Interaction with the outside world
 - I Magnet, accelerator system, safety system, etc.

LHCP ECS Scope



Clara Gaspar, September 2001

LACE ECS Requirements

Integrate the different activities

I Such that rules can be defined (ex: Stop DAQ when DCS in Error)

Allow Stand-alone control of sub-systems

I For independent development and concurrent usage.

Automation

Avoids human mistakes and speeds up standard procedures

Easy to operate

I Two to three operators (non-experts) should be able to run the experiment.

Scalable & Flexible

Allow for the integration of new detectors

Maintainable

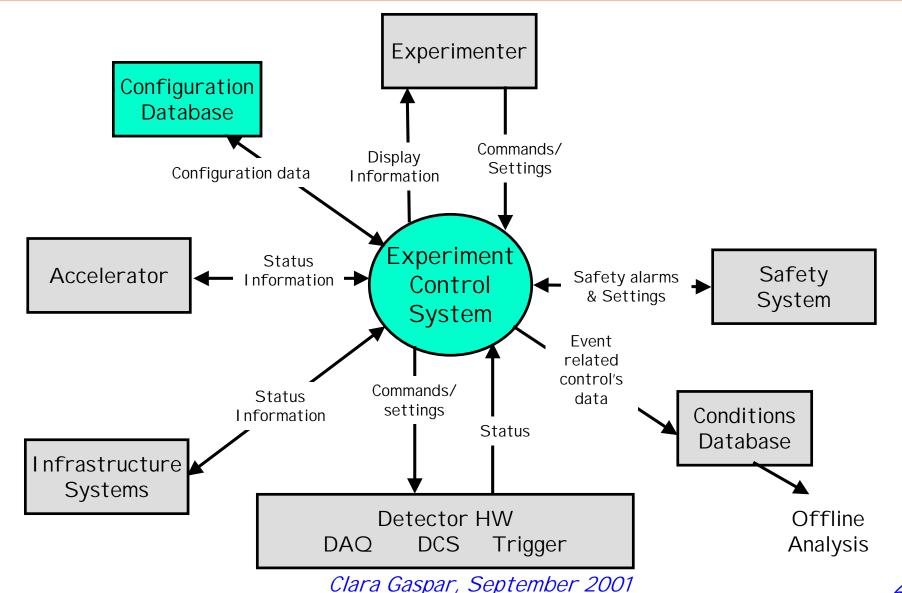
I Experiments run for many years

Kick Experiment Control

Keyword: Homogeneity

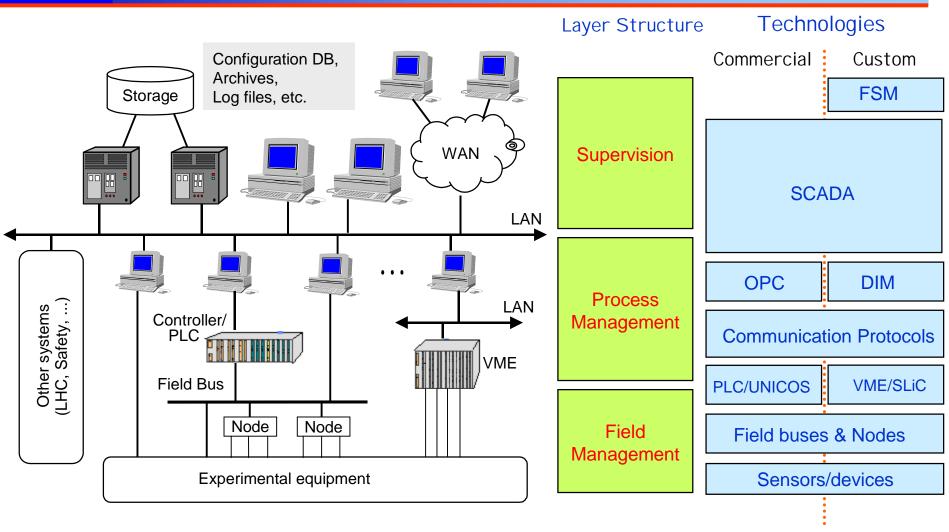
- A Common Approach in the design and implementation of all parts of the system:
 - I Facilitates inter-domain integration
 - Makes it easier to use:
 - Standard features throughout the system (ex: partitioning rules)
 - Uniform Look and Feel
 - Allows an easier upgrade and maintenance
 - I Needs less manpower

Kick ECS Context Diagram

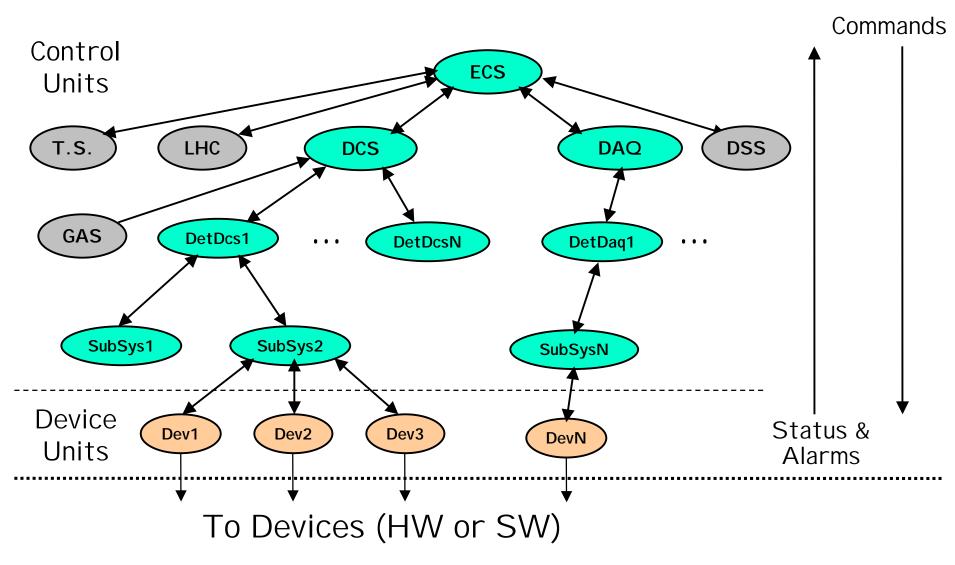


- Integrated approach from design phase
- An Architecture
 - That can handle all aspects of the monitoring and control of the Experiment
- A framework
 - A collection of tools and mechanisms that allow the implementation of the architecture

Hees HW Architecture



Kack Generic SW Architecture



Clara Gaspar, September 2001

Heb The Framework

- An integrated collection of guidelines, tools and components
- Should be provided to sub-system developers in order to:
 - Allow the development of each component coherently in view of its integration in the complete system.
 - For the two types of components:
 - DCS

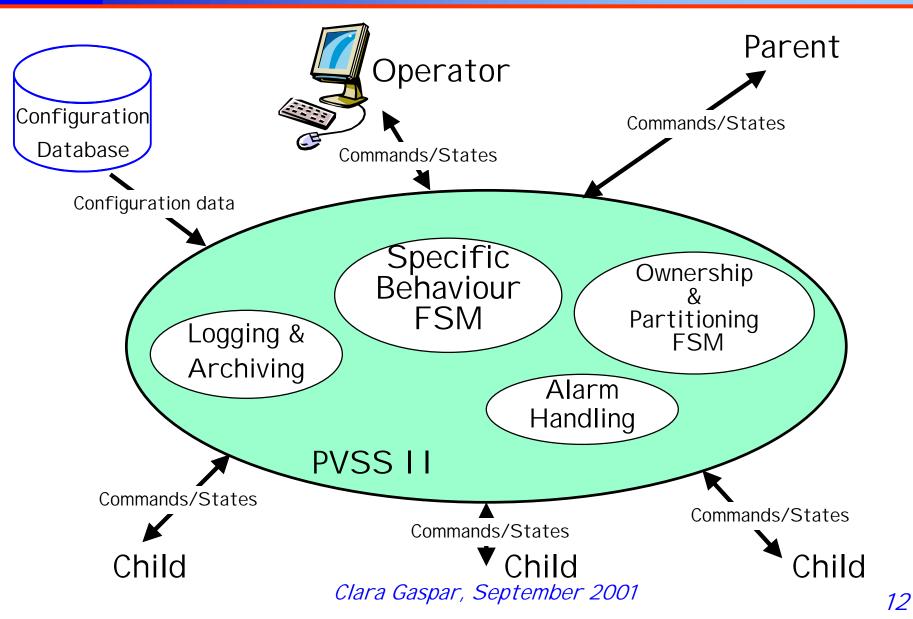
Dev1

- Control Units
- Device Units

Each CU is inherently able to:

- Configure, monitor and control its children
 - I Sequence & Automate operations
 - I Recover errors
- Handle Alarms
 - I Filter and display alarms
- Partition
 - I Exclude one or more of its children
- User Interfacing
 - I Present information and receive commands

LHCB Control Units (cont.)



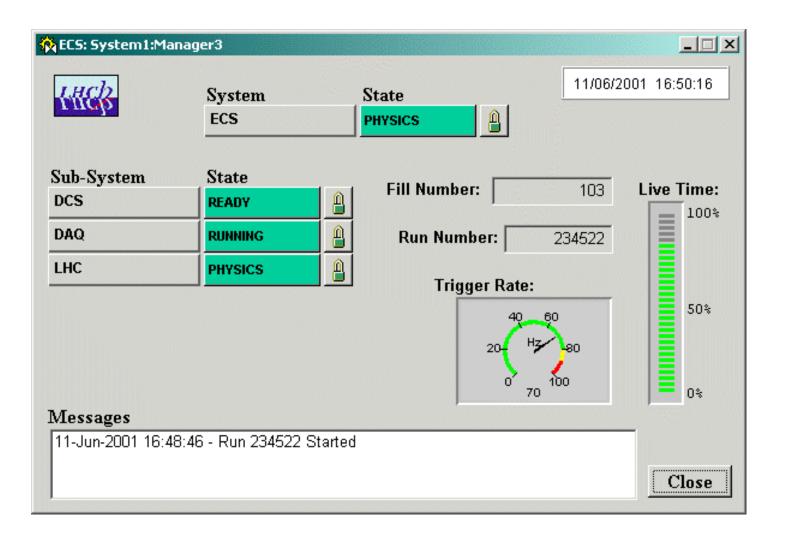
Each Control Unit (and its sub-tree)

- Can run in stand-alone
- Can be controlled independently (by an authorized User Interface)

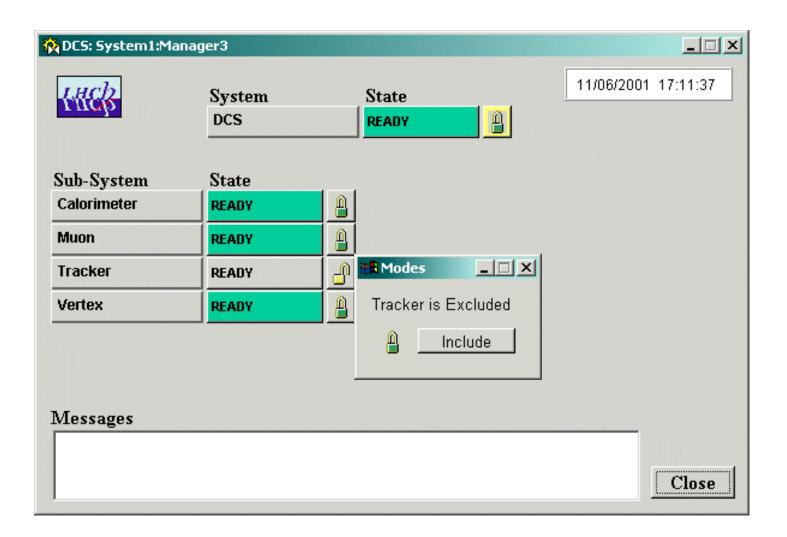
Run Control

- Is a particular instance of a user interface:
- It is the interface to the Root of the tree
- If the tree is partitioned there can be several Run Controls.

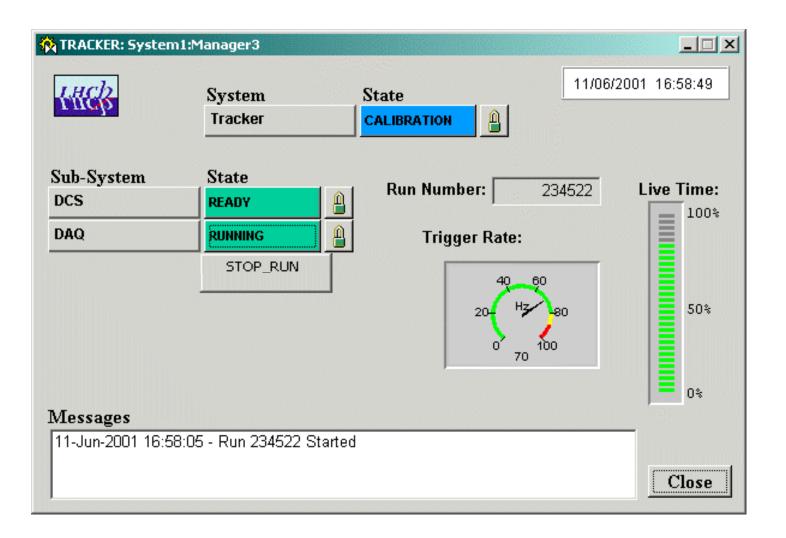
KHCP Run Control



Hes Partitioning Sub-Systems



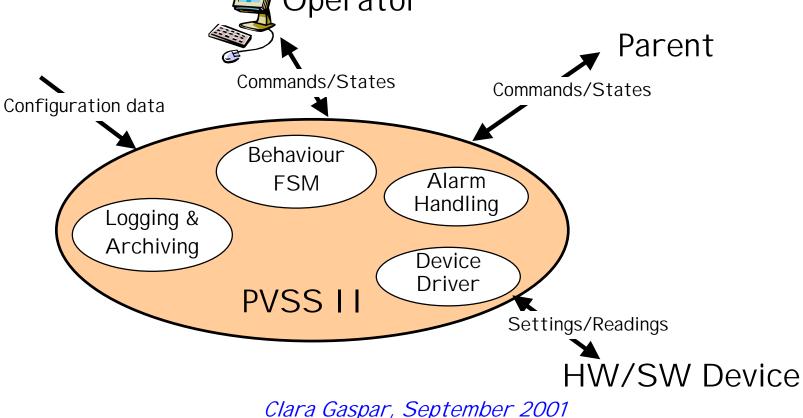
Kick Sub-detector Control



Luck Device Units

Device Units

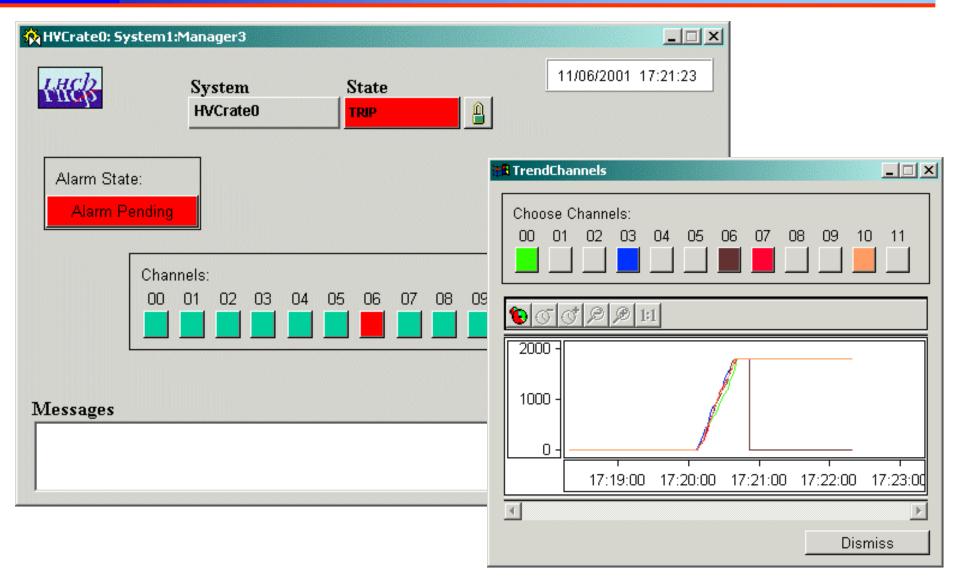
Provide the interface to the different devices
Operator



LHCB DCS Devices

- High Voltages, Low Voltages, etc.
 - Try to use Commercial HW
 - Fieldbuses: CAN, Profibus, WorldFip, Ethernet
 - And OPC Servers (provided by manufacturer)
- Analog & Digital IO (temperatures, humidities, etc.)
 - Same as above, or
 - ELMBs / OPC Servers
- Others:
 - PLCs / OPC Servers
 - Home made / DIM or OPC Servers

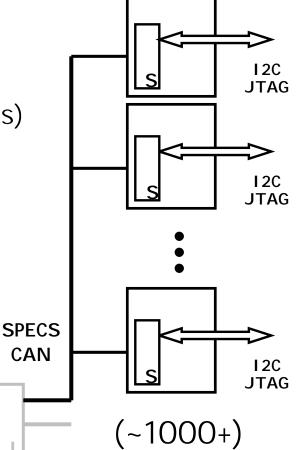
Heb HV Device



Kick Front-End Electronics

In Radiation Areas

- Mainly needed I²C and JTAG
 - SPECS (ex. SPAC)
 - Serial Protocol (inspired from Atlas)
 - 10Mb/s
 - Slave is radiation tolerant
 - I ELMB
 - CAN protocol (0.5Mb/s)

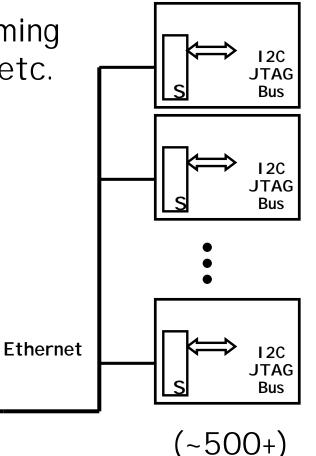


Clara Gaspar, September 2001

Hees Board level electronics

Electronics in barracks

- I Front-ends, Readout Units, Timing and Fast Control components, etc.
- Credit Card PC's
 - $1.66 \times 85 \times 12 \text{ mm}^3$
 - I Pentium Compatible CPU
 - Linux/DIM (no PVSS)





Kick Event Filter Farm Control

 Remote Boot

 SW download

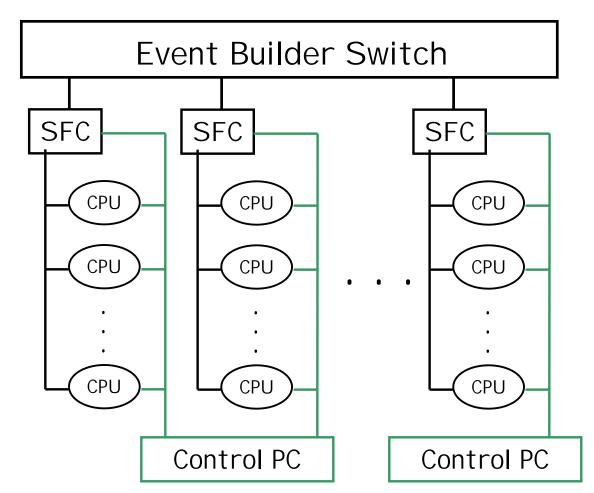
 CPU Monitoring

 CPU usage, memory, IO, etc.

 Processes

 (trigger algorithms)
 Configure

- I Monitor
- I Start/Stop



LHCD CPU Monitoring Using PVSS II

| sion_1: pcmon.pnl Panel <u>?</u> | | | | _ | X |
|-------------------------------------|-----|-----|----------|----------|--|
| | | | | | Reformance |
| cepdelpO1 - |] | | PC | EPDELP01 | PCEPDELP01 CPU Time (%) |
| Image Name | PID | CPU | CPU Time | Memory | CCCPP II |
| System | 2 | 1 | 00:03:33 | 200 K | |
| smss.exe | 20 | 0 | 00:00:00 | 20 K | 80 - 1 (|
| winlogon.exe | 34 | 0 | 00:00:00 | 36 K | |
| services.exe | 40 | 0 | 00:00:04 | 1768 K | 40 - |
| lsass.exe | 43 | 0 | 00:00:00 | 636 K | |
| spoolss.exe | 68 | 0 | 00:00:01 | 480 K | 13:39:00 13:40:00 13:41:00 13:42:00 13:43:00 |
| testloop.exe | 69 | 100 | 00:00:16 | 40 K | |
| RpcSs.exe | 83 | 0 | 00:00:00 | 664 K | |
| inetd32.exe | 89 | 0 | 00:00:00 | 76 K | Memory Usage (Kb) |
| rtvscan.exe | 92 | 0 | 00:24:33 | 3216 K | |
| lprserv.exe | 98 | 0 | 00:00:05 | 436 K | 120000 - |
| msiexec.exe | 106 | 0 | 00:00:00 | 308 K | |
| pstores.exe | 111 | 0 | 00:00:00 | 68 K | |
| MSTask.exe | 114 | 0 | 00:00:00 | 132 K | 40000 - |

|

60424 K

KKCS Control System Monitoring

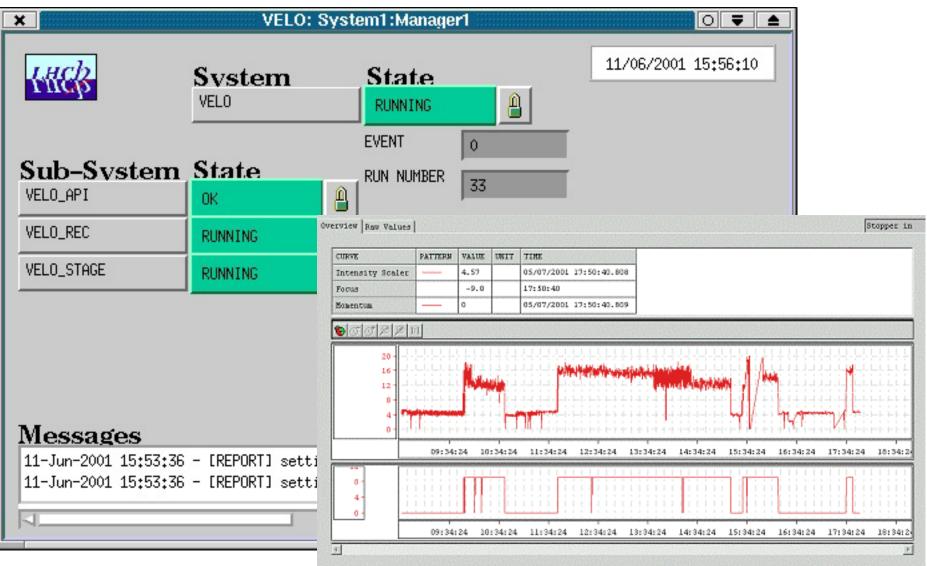
- The Monitoring of the Control System itself is very similar to the Monitoring of the CPUs in the Event Filter Farm:
 - Controls PCs, Credit Card PC's, etc:
 - Remote Boot & SW download
 - I CPU Monitoring
 - CPU usage, memory, IO, etc.
 - Processes (controls processes: PVSS, etc.)
 - Configure, Monitor, Start/Stop

Kick Proof of Concept

TestBeam Setup:

- Run Control based on JCOP framework
 Interfacing to Cascade DAQ
- Integrated PS/(SPS) information
- Started integrating DCS devices:
 - I HVs
 - I motors

LHCS Test Beam Run Control



Kick Related Projects

- Our Control System relies on the success of the following (common) projects:
 - JCOP Framework
 - GAS Control Working Group
 - JCOV Joint Cooling and Ventilation Control
 - DSS Detector Safety System
 - Magnet Control
 - Rack Control
 - Data interchange Working Group (LHC,T.S.,Exps,etc.)

Hes Conclusions (1)

- The best way to achieve an homogeneous and maintainable control system (and to save manpower) is:
 - I To do the maximum in common
 - The Controls Framework is being developed and will be used by the 4 LHC experiments (Joint Controls Project)
 - New "Devices" should be developed in a re-usable way and included in the Framework
 - I To Standardize on HW choices as much as possible
 - So that common SW can be used
- Please contact us for HW choices
 - (of potentially common items)
 - like: power supplies, temperature sensors, etc

Kick Conclusions (2)

- In order to design and implement the LHCb Control System we have the following resources:
 - Clara Gaspar
 - Sascha Schmeling (fellow, prob. leaving soon)
 - Richard Beneyton (cooperand, leaving soon)
 - Boda Franek (Rutherford, at 25 %)
- We have many interesting projects to offer...